

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Adiletta et al.

Art Unit : 2183

Serial No.: 10/615,500

Examiner : Ellis, Richard L

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Assignee : Intel Corporation

Title : PARALLEL PROCESSOR ARCHITECTURE

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF DECEMBER 23, 2005

Claim Listing/Amendments

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Remarks

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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